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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Boerstler

§ Group Art Unit: 2634

Serial No.: 09/666,277

§ Examiner: Pathak, Sudhanshu C.

Filed: September 21, 2000

§ Attorney Docket No.: AUS9-2000-0240-US1

For: Method and System for  
Clock/Data Recovery for Self-  
Clocked High Speed Interconnects

§

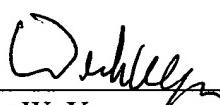
**LETTER TO THE OFFICIAL DRAFTSMAN**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith are formal drawings (11 sheets) for the above-identified application.

Respectfully submitted,

  
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